

Amendment of Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1-10 (canceled)

11. (currently amended) A module for a display device comprising:
a wiring substrate having a single level of wiring thereon;
a plurality of integrated circuits mounted on the wiring substrate in juxtaposition, each integrated circuit comprising a switching circuit having inputs coupled to n input terminals (where n is a natural number and $n \geq 2$) to receive data signals, the switching circuit generating m output signals (where m is a natural number ≥ 2) coupled to a drive signal generation circuit for driving the display device each of the integrated circuits having the inputs arranged linearly in a row along a first side and the output on a second side parallel to the first side, the second side facing the display device, the first side facing away from the display device, the switching circuit sequentially connecting the first through n -th input terminals to the first through m -th output terminals respectively when a control signal is at a first logical level and sequentially connecting the first through n -th input terminals to the m -th through first output terminals, respectively when the control signal is at a second logical level;
wherein wiring on the wiring substrate is connected to the n input terminals to couple data signals to the inputs of the switching circuits, the wiring being parallel lines.

12. (canceled)

13. (previously added) The module of Claim 11 wherein the wiring substrate is a flexible substrate.

14. (currently amended) The module of Claim 11 wherein the wiring for the first integrated circuit approaches the integrated circuit on the wiring substrate from a first direction and the wiring for the second integrated circuit approaches the integrated circuit approaches the integrated circuit on the wiring substrate from a second direction ~~perpendicular~~ opposite to the first direction.

15. (currently amended) The module of Claim 11 wherein wiring between the n input terminals and the switching circuit comprise a continuous ~~loop~~ line between a first terminal, an input to the switching circuit and a second input terminal.

16. (previously added) The module of Claim 12 wherein the wiring substrate is a flexible substrate.

17. (currently amended) The module of Claim 12 wherein the wiring for the first integrated circuit approaches the integrated circuit on the wiring substrate from a first direction and the wiring for the second integrated circuit approaches the integrated circuit approaches the integrated circuit on the wiring substrate from a second direction ~~perpendicular~~ opposite to the first direction.

18. (currently amended) The module of Claim 13 wherein the wiring for the first integrated circuit approaches the integrated circuit on the wiring substrate from a first direction and the wiring for the second integrated circuit approaches the integrated circuit approaches the integrated circuit on the wiring substrate from a second direction ~~perpendicular~~ opposite to the first direction.

19. (currently amended) The module of Claim 12 wherein wiring between the n input terminals and the switching circuit comprise a continuous ~~loop~~ line between a first terminal, an input to the switching circuit and a second input terminal.

20. (currently amended) The module of Claim 13 wherein wiring between the n input terminals and the switching circuit comprise a continuous ~~loop~~ line between a first terminal, an input to the switching circuit and a second input terminal.

21. (currently amended) The module of Claim 14 wherein wiring between the n input terminals and the switching circuit comprise a continuous ~~loop~~ line between a first terminal, an input to the switching circuit and a second input terminal.

22. (previously added) The module of Claim 11 wherein the control signal coupled to the plurality of integrated circuits is at the first logic level for one integrated circuit of a pair of integrated circuits and is at the second logic level for another integrated circuit of the pair.

23. (canceled)

24. (previously added) The module of Claim 22 wherein the wiring substrate is a flexible substrate.

25. (currently amended) The module of Claim 22 wherein the wiring for the first integrated circuit approaches the integrated circuit on the wiring substrate from a first direction and the wiring for the second integrated circuit approaches the integrated circuit approaches the integrated circuit on the wiring substrate from a second direction ~~perpendicular~~ opposite to the first direction.

26. (currently amended) The module of Claim 22 wherein wiring between the n input terminals and the switching circuit comprise a continuous ~~loop~~ line between a first terminal, an input to the switching circuit and a second input terminal.